

### **TSS4B01G - TSS4B04G**

.602(15.3) .579(14.7)

.122(3.1)

.068(1.73)

.030(0.75)

.022(0.55)

.709(18)

,669(17)

303(7,7)

.287(7.3)

.147(3.7)

Single Phase 4.0 AMPS. Glass Passivated Bridge Rectifiers

.996(25.3) .972(24.7)

.118(3,0)X45

.043(1.1)

.035(0.9)

## TS4B







#### **Features**

- ♦ UL Recoganized File # E-326243
- ♦ Glass passivated junction
- ♦ Ideal for printed circuit board
- ♦ Reliable low cost construction
- Plastic material has Underwriters Laboratory Flammability Classification 94V-0
- ♦ Low Forward Voltage Drop.
- → High case dielectric strength of 2000V<sub>RMS</sub>
- → High temperature soldering guaranteed: 260°C / 10 seconds at 5 lbs., (2.3kg) tension
- Green compound with suffix "G" on packing code & prefix "G" on datecode.

# TSS4B0XG SGYWW

# <u>Dimensions in inches and (millimeters)</u> Marking Diagram

.087(2.2) .071(1,8)

> TSS4B0XG = Specific Device Code G = Green Compound

Y = Year WW = Work Week

### **Mechanical Data**

♦ Case: Molded plastic

 Terminals: Leads solderable per MIL-STD-750, Method 2026

♦ Weight: 4 grams

♦ Mounting torque : 5 in. lbs. Max.

### **Maximum Ratings and Electrical Characteristics**

Rating at 25 °C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

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Type Number	Symbol	TSS4B 01G	TSS4B 02G	TSS4B 03G	TSS4B 04G	Units
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	V
Maximum RMS Voltage	$V_{RMS}$	35	70	140	280	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	200	400	V
Maximum Average Forward Rectified Current @ $T_c$ =100 $^{\circ}$ C	I <sub>F(AV)</sub>	4				А
Peak Forward Surge Current, 8.3 ms Single Half Sinewave Superimposed on Rated Load (JEDEC method)	ICCM	150				А
Maximum Instantaneous Forward Voltage (Note 1) @ 4 A	V <sub>F</sub>	0.98		1.3	V	
Maximum Reverse Recovery Time(Note 2)	Trr	35 50			50	nS
Maximum DC Reverse Current @ $T_A$ =25 $^{\circ}$ C at Rated DC Blocking Voltage @ $T_A$ =125 $^{\circ}$ C		5 500				uA uA
Typical Thermal Resistance (Note 3)	$R_{\theta JC}$	5.5				°C/W
Operating Temperature Range	TJ	- 55 to + 150				οС
Storage Temperature Range	T <sub>STG</sub>	- 55 to + 150				οС

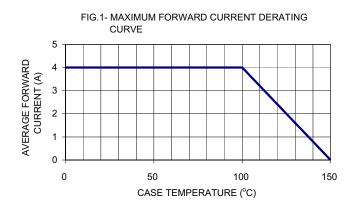
Note 1 : Pulse Test with PW=300 usec, 1% Duty Cycle

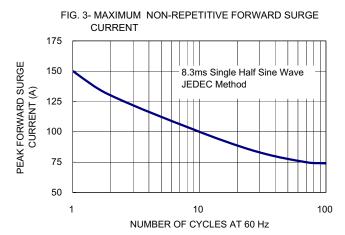
Note 2 : Reverse Recovery Test Conditions:  $I_F$ =0.5A,  $I_R$ =1.0A,  $I_{RR}$ =0.25A.

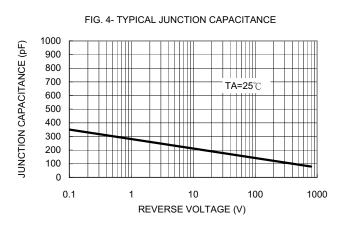
Note 3: Mounted on 2" x 3" x 0.25" Al-Plate Heatsink.

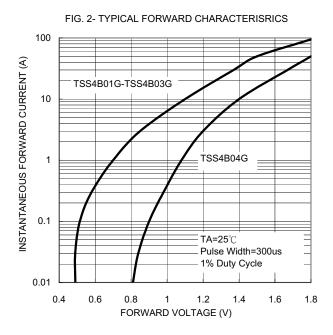


### RATINGS AND CHARACTERISTIC CURVES (TSS4B01G THRU TSS4B04G)









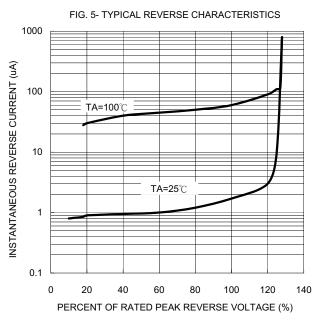


FIG.6- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

