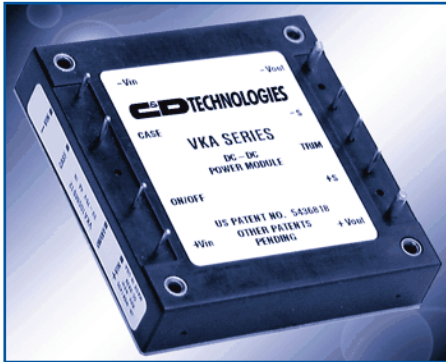


VKA50xS

50 Watt Single Output Half Brick DC/DC Converter



- 18-36 V & 33 - 75V Input Range
- High Efficiency: 87% Typical at 5V
- 100mS Transient Response 50-100% Load Step
- 420 kHz Fixed-Frequency Operation
- Remote Sense
- Operation to +100°C Baseplate Temperature
- Primary Remote On/Off, Choice of Pos/Neg Logic
- Adjustable Output Voltage
- Continuos Short-Circuit Protection
- Thermal Shutdown



The VKA50xS Series DC/DC converters present an economical and practical solution for distributed power system architectures which require high power density and efficiency while maintaining system modularity and upgradeability. With the ability to operate over a wide input voltage range of 18 to 36 and 33 to 75 volts, these modules are ideal for use in

battery backup applications common in today's telecommunication and electronic data processing applications. The output is fully isolated from the input, allowing for a variety of polarity and grounding configurations.

The VKA50xS's proprietary control circuitry responds to 50-100% load steps in 100mSeconds to within 1% nominal Vout.

The patented fixed frequency architecture combined with surface mount technology results in a compact, efficient and reliable solution to DC/DC conversion requirements. Safety per UL1950, EN 60950 and CSA 22.2 #234

PRODUCT SELECTION CHART

MODEL	INPUT VOLTAGE	VOUT (VDC)	IOUT (A)	EFFICIENCY	
				MIN	TYP
VKA50LS03		3.3V	10.0	80	81
VKA50LS05	24VDC	5.0V	10.0	85	86
VKA50LS12		12.0V	4.2	87	88
VKA50LS15	(18-36)	15.0V	3.3	88	89
VKA50LS24		24.0V	2.1	89	90
VKA50MS03		3.3V	10.0	81	82
VKA50MS05	48VDC	5.0V	10.0	86	87
VKA50MS12		12.0V	4.2	88	89
VKA50MS15	(33-75)	15.0V	3.3	89	90
VKA50MS24		24.0V	2.1	89	90

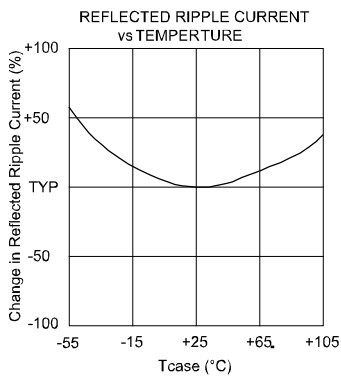
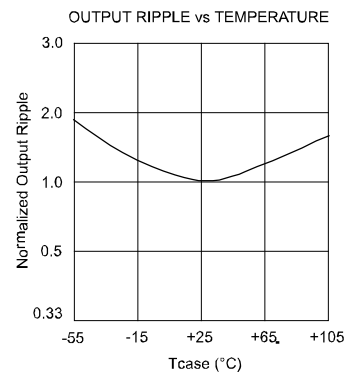
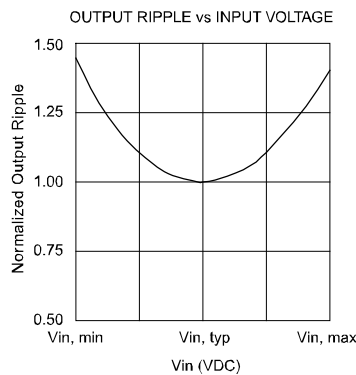
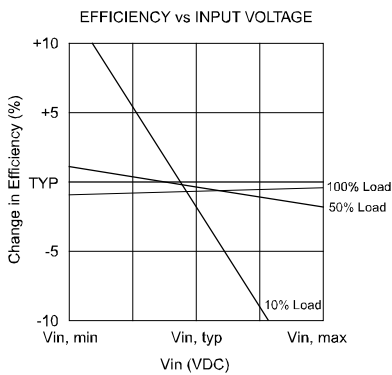
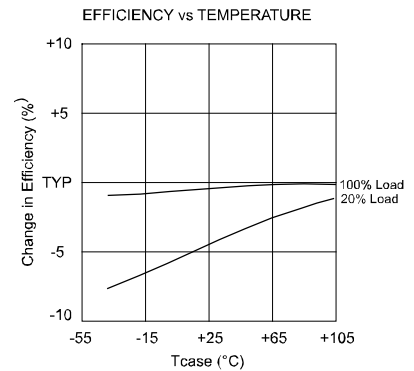
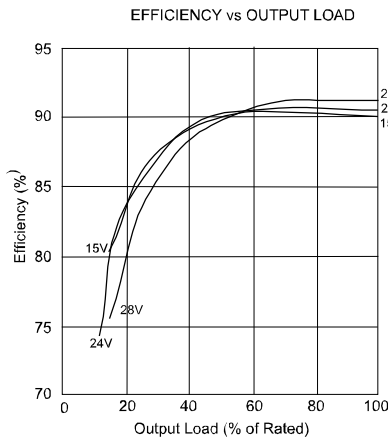
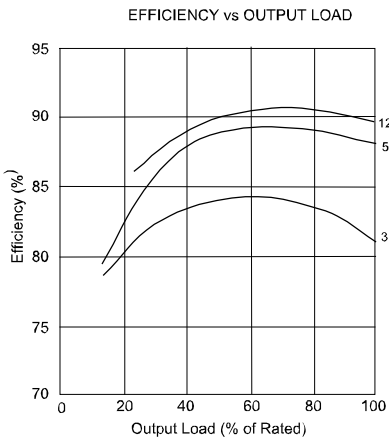
SPECIFICATIONS, ALL MODELS

Specifications are at $T_{CASE} = +40^{\circ}C$ nominal input voltage unless otherwise specified.

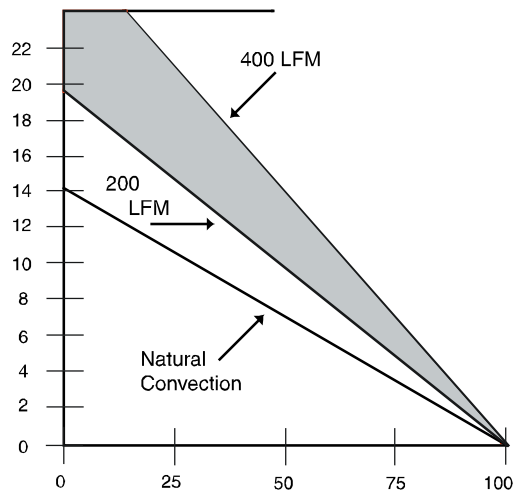
	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
INPUT	INPUT					
	Voltage Range					
	VKA50LS		18	24	36	VDC
	VKA50MS		33	48	75	VDC
	Maximum Input Current					
	VKA50LS	$V_{IN} = 16VDC$			3.7	A
	VKA50MS	$V_{IN} = 27VDC$			2.2	A
	Reflected Ripple Current	Peak - Peak		20		mA
	Input Ripple Rejection	DC to 1KHz	50	60		dB
	No Load Input Current LS/MS			50/100		mA
	No Load Standby, Primary On/Off Disabled LS/MS	Power Dissipation LS/MS		3.6/4.8 0.18/0.4		W W
	Inrush Charge	$V_{IN} = V_{IN} max.$				
VKA50LS				0.520	mC	
VKA50MS				0.360	mC	
Quiescent Operating Current			8	12	mA	
Primary On/Off Disabled						
OUTPUT	OUTPUT					
	Rated Power		0		50	W
	Set point Accuracy				1	%
	Line Regulation	High Line to Low Line		0.02	0.05	%
	Load Regulation	No Load to Rated Load		0.2	0.5	%
	Output Temperature Drift			± 0.02		$\%/^{\circ}C$
	Output Ripple, p-p	DC to 20MHz BW		1%		$V_{OUT} Nom$
	Output Current Limit Inception			130%	150%	$I_{OUT} Nom$
	Output Short-Circuit Current (2)	test		120%	150%	$I_{OUT} Nom$
	Output Overvoltage Limit			125%	135%	V
	Transient Response	50 to 100% Load Step				
	Peak Deviation	$di/dt = 1.0A/\mu Sec$		2%		$V_{OUT} Nom$
Settling Time	$V_{OUT} 1\%$ of Nominal Output		100		μSec	
GENERAL	ISOLATION					
	Input to Output	Peak Test for 2 Seconds	1500			VDC
	Input to Baseplate		1500			VDC
	Output to Baseplate		500			VDC
	Resistance		10			M Ω
	Capacitance			2000		pF
	Leakage Current	$V_{ISO} = 240VAC, 60Hz$		180		$\mu A, rms$
	GENERAL					
	Efficiency, Line, Load, Temp. (3)					
	Switching Frequency		400	420	440	KHz
	Remote Sense Compensation				0.5	V
	Output Voltage Adjust Range	12V & higher(4)		-50% / +25%		$V_{OUT} Nom$
	Remote On/Off Control Inputs					
	Primary	Open Collector/Drain				
	Sink Current-Logic Low				1.0	mA
	Vlow				0.4	V
	Vhigh				Open Collector	
	Turn-on Time	Within 1% of Rated Output		10.0	12.5	mSec
	Weight				85 (3.0)	g (oz.)
	TEMPERATURE					
	Operation/Specification	Case Temperature	-40	+25	+100	$^{\circ}C$
	Storage	Case Temperature	-55	+25	+125	$^{\circ}C$
Shutdown Temperature	Case Temperature	+100		+115	$^{\circ}C$	
Thermal Impedance, case-ambient			7.1		$^{\circ}C/W$	
Lead Solder Temperature	10 Seconds max			+300	$^{\circ}C$	

- NOTES:**
- 1) See Typical Performance Curves, page 3
 - 2) Continuous Mode
 - 3) See graphs for Efficiency vs. Output Load, V_{IN} , T_{CASE}
 - 4) 3.3V Models Limited in Trim Down Range
 - 5) Consult Factory for Details

TYPICAL PERFORMANCE CURVES
T_{CASE} = +40°C nominal input voltage unless otherwise specified.



POWER DERATING WITH NO HEATSINK



ORDERING INFORMATION

Device Family VKA50 xSzz -
 Indicates 50 Watt Regulated Unit

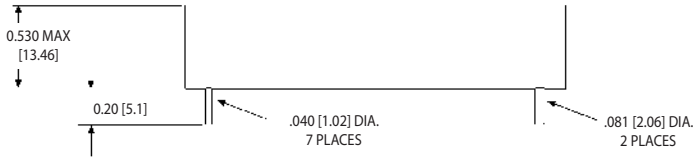
Model Number _____
 Selected from Table of Electrical Characteristics
 Where:
 x = Input Voltage (L = 24VDC; M = 48VDC)
 zz = Output Voltage (03=3.3V, 05=5V, etc.)

Lead Length _____
 0.200" - No Number
 0.145" - (6)
 0.110" - (8)

Remote On-Off Logic: _____
 Positive - No Number
 Negative - (1)

MECHANICAL

SIDE VIEW



NOTES:

All dimensions are in inches (millimeters).

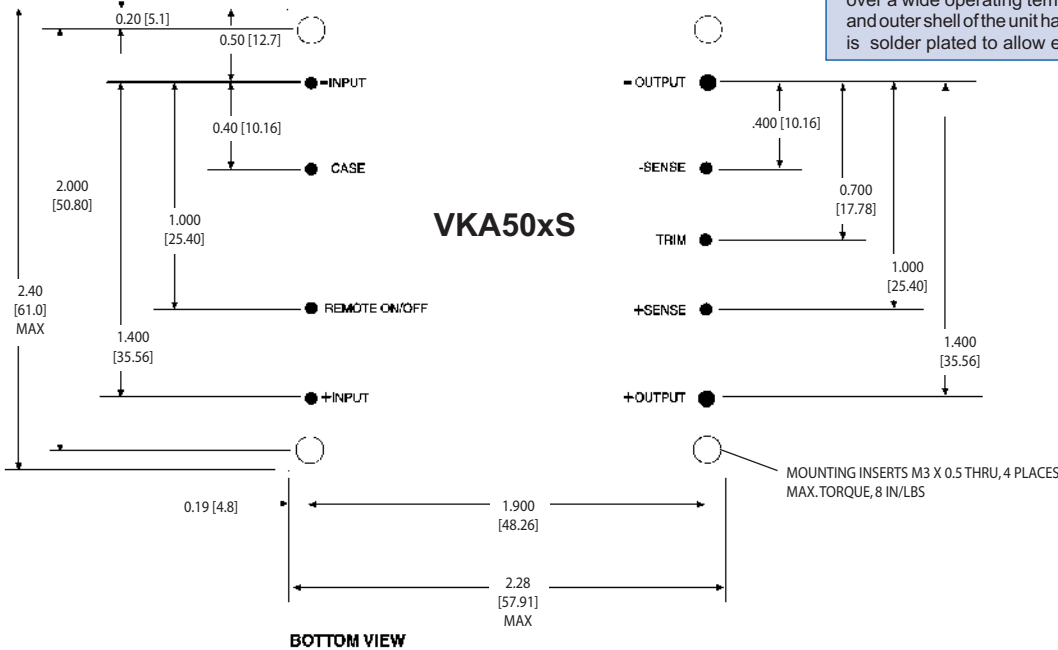
PIN PLACEMENT TOLERANCE: ± 0.005 "

MECHANICAL TOLERANCE: ± 0.015 "

Marked with: specific model, ordered, date code, job code.

MATERIAL: Units are encapsulated in a low thermal resistance molding compound which has excellent chemical resistance and electrical properties in high humidity environments and over a wide operating temperature range. The encapsulant and outer shell of the unit have UL94V-0 ratings. Lead material is solder plated to allow ease of solderability.

VKA50xS



BOTTOM VIEW

OUTPUT ADJUST VOLTAGE

This feature allows the user to accurately adjust the module's output voltage set point to a specified level. This is achieved by connecting a resistor or potentiometer from the TRIM terminal to either the +V_{out} terminal (for increased V_{out}) or the -V_{out} terminal (for decreased V_{out}). The formulae below describe the trim resistor value to obtain a V_{out} change of D%. V_o is output voltage prior to adjustment (3.3V, 5V, 12V, 15V, or 24V).

$$\text{Radj - up} = \left(\frac{V_o(100 + D\%)}{1.225D\%} - \frac{(100 + 2D\%)}{D\%} \right) \text{k}\Omega$$

$$\text{Radj - down} = \left(\frac{100}{D\%} - 2 \right) \text{k}\Omega$$

OVP NOTE

Special attention should be given to the peak voltage deviation during a dynamic load step when trimming the output above the original set point to avoid tripping the overvoltage protection circuit. Should an OVP condition occur, the converter will go into a latch condition and must be externally reset before it will return to normal operation.

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